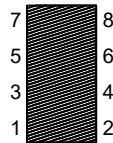


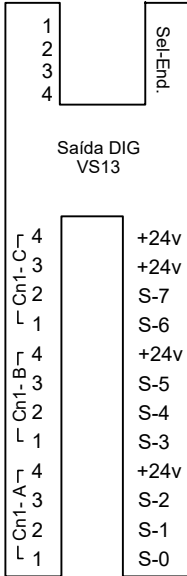
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**TOPO  
S-DIG**



**Saída DIG**

**BORNE  
S-DIG**



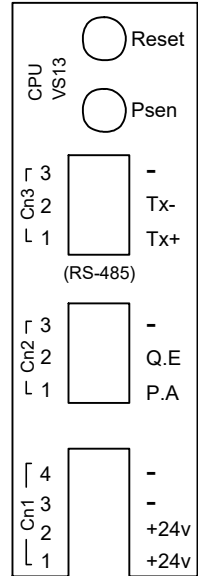
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**TOPO  
CPU**



**CPU RS-232**

**BORNE  
CPU**



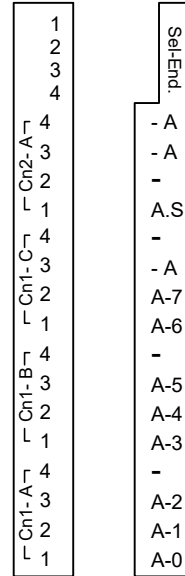
**Entrada ANL**

**TOPO  
E-ANL**



**Entrada ANL**

**BORNE  
E-ANL**



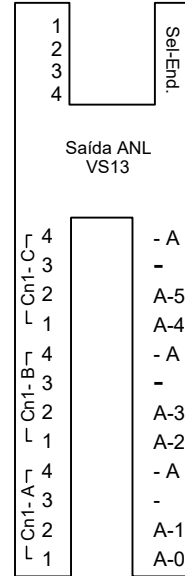
**Saída ANL**

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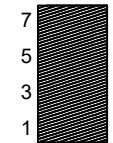
**Saída ANL**

**BORNE  
S-ANL**



**Entrada DIG**

**TOPO  
E-DIG**



**Entrada DIG**

**BORNE  
E-DIG**

